

FIG. 1

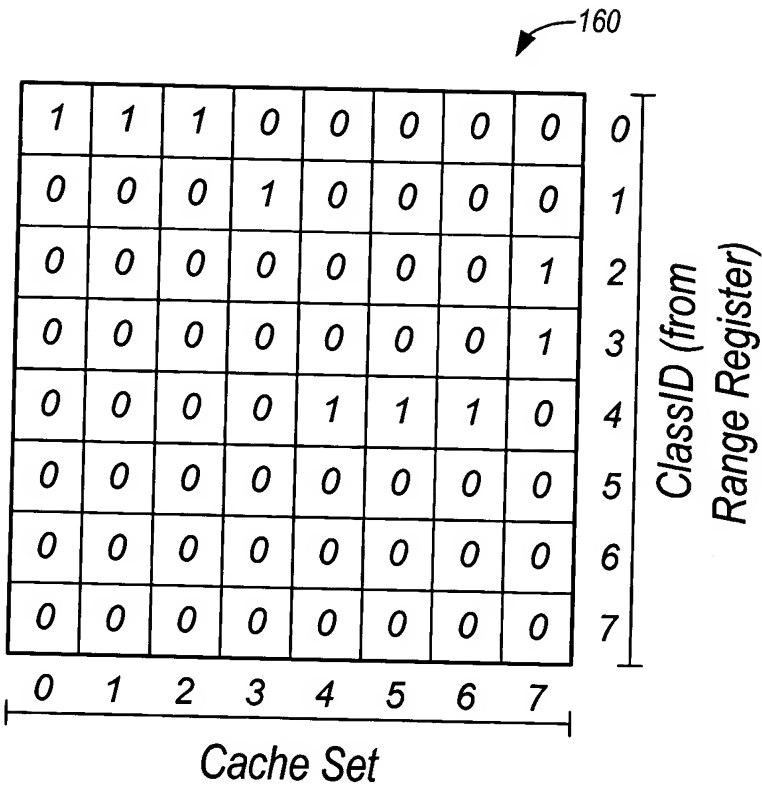


FIG. 2

Replacement Management Table:				
RMT_index	RMT_entry			
RMT_index 0	Set Enable Bits	v	b	a
RMT_index n	Set Enable Bits	v	b	a

FIG. 3

400

[illegible]

FIG. 4

500

[illegible]

FIG. 5A

Class/D Register (CIDR)

[illegible]

FIG. 5B

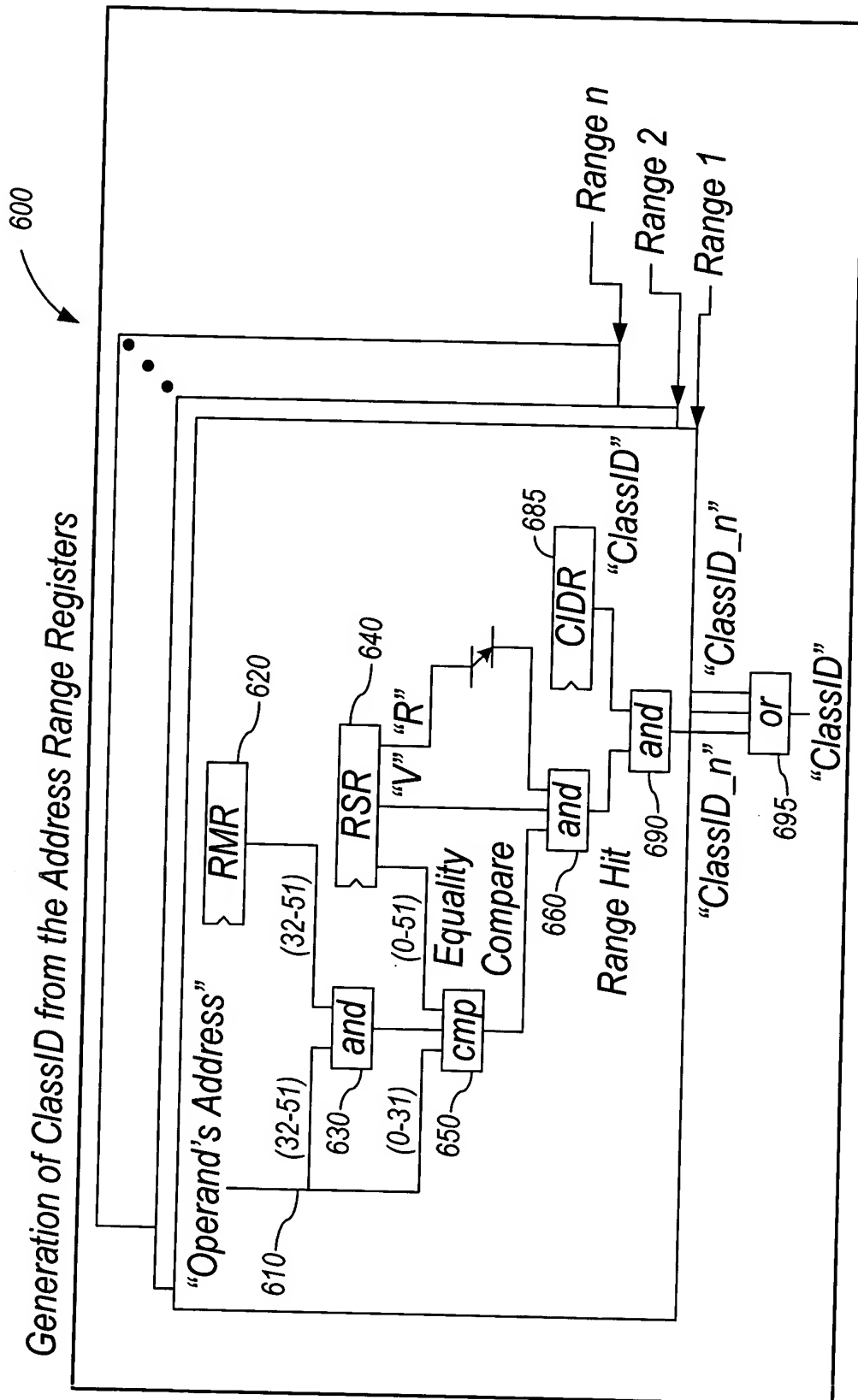


FIG. 6

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